



# NSQF LEVEL 5 CERTIFICATION COURSE

Advance your career prospects in the VLSI and semiconductor domain with our specialized training program.

- **Starting Date: March 2<sup>nd</sup>, 2026**
- **Duration: 780 hours**
- **Course Fee: ₹ 41,120 (General) (50% Off for General Students)**
- **FREE for SC/ST candidates**
- **Nationally recognized certification**
- **Job placement assistance**

**APPLY BEFORE: February 27th, 2026**

Limited seats available (20 only)!

Register now to secure your future in chip design!

<https://nielitbhubaneswar.in/admin/./student/register.php?course=CDAa-2026>

**REGISTER NOW**



# JUNIOR CHIP DESIGNER

(A-Level 'Chip Design')

**Course Code:**

**QG-05-EH-02594-2024-V1-NIELIT**

#### LEARN FROM EXPERTS:

- VLSI Verification Fundamentals and VLSI circuits design for testability.
- Static Timing Analysis
- Verilog RTL Coding for Synthesis
- FPGA Architecture & Programming
- Hands-on projects using industry tools like Xilinx, Vivado and Cadence
- ASIC Verification using System Verilog and UVM
- Accelerator design using HLS programming and SOC Design and Verification

#### CAREER OPPORTUNITIES:

- Application Engineer
- FPGA Developer
- RTL Design Engineer
- Embedded Systems Engineer

#### ELIGIBILITY:

- Completed UG Diploma in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches
- Completed 3 Years of Diploma in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches after class 10th & having an experience of 1.5 Years
- Completed 2nd Year of Diploma in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches after class 12th
- Acquired NSQF Level 4 in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches and having an experience of 3 Years in relevant sector
- Acquired NSQF Level 4.5 in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches and having an experience of 1.5 years in relevant sector

#### Course Coordinator:

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