MINISTRY OF
**ELECTRONICS &
INFORMATION TECHNOLOGY**
GOVERNMENT OF INDIA
सत्यमेव जयते

रा.इ.सू.प्रौ.सं
**NIELIT
BHUBANESWAR**

NSQF LEVEL 4 CERTIFICATION COURSE

CHIP DESIGN ASSOCIATE
(O-Level Chip Design)

Course Code: QG-04-EH-02593-2024-V1-NIELIT

An intensive three-month gateway designed to position aspiring professionals at the forefront of the VLSI and semiconductor industry.

- Starting Date: March 2nd, 2026
- Duration: 450 hours/3 months
- Course Fee: ₹ 24,150 (General)
  (50% Off for General Students)
- FREE for SC/ST candidates
- Nationally recognized certification
- Job placement assistance

APPLY BEFORE: February 27th, 2026

Limited seats available (20 only)!
Register now to secure your
future in chip design!

🌐 https://nielitbhubaneswar.in/admin/../student
/register.php?course=CDA-2026

REGISTER NOW

LEARN FROM EXPERTS:
- VLSI Fundamentals
- Static Timing Analysis
- Hands-on projects using industry tools like Xilinx Vivado
- Verilog RTL Coding for Synthesis
- FPGA Architecture & Programming

CAREER OPPORTUNITIES:
- Front-End VLSI Design Engineer
- FPGA Developer
- RTL Design Engineer

ELIGIBILITY:
- Completed 2nd Year of 3 Years Diploma in Electronics and communication Engineering/ Electrical Engineering/CS/IT and allied branches after class 10th
- Completed 12th or equivalent in Science with Physics and Maths.
- Acquired NSQF Level 3.5 in Electronics and Communication engineering/Electrical Engineering/CS/IT and allied branches with 1.5 experience.
Acquired NSQF Level 3 in Electronics and Communication engineering Electrical Engineering/CS/IT and allied branches with 3 years experience.

Course Coordinator:

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