MINISTRY OF
**ELECTRONICS &
INFORMATION TECHNOLOGY**
GOVERNMENT OF INDIA
सत्यमेव जयते

रा.इ.सू.प्रौ.सं
NIELIT
BHUBANESWAR

NSQF LEVEL 4 CERTIFICATION COURSE

Fundamentals of VLSI Design

Course Code: NG-04-EH-02901-2024-V1-NIELIT

From Gate to Global Interconnect: An Advanced Three-Week Program to Bridge the Gap Between Theory and Tape-Out.

- **Starting Date: March 2nd, 2026**
- **Duration: 60 hours/3 weeks**
- **Course Fee: ₹ 3,740 (General)**
- **FREE for SC/ST candidates**
- **Nationally recognized certification**
- **Job placement assistance**

APPLY BEFORE: February 27th, 2026

Limited seats available (20 only)! Register now to secure your future in chip design!

🌐 https://nielitbhubaneswar.in/admin/../student /register.php?course=FVD-2026

REGISTER NOW

LEARN FROM EXPERTS:

- **FULL-CYCLE VLSI DESIGN FLOW**
- **CMOS & MOSFET FUNDAMENTALS**
- **TRANSISTOR-LEVEL LAYOUT DESIGN**
- **TIMING & INTERCONNECT DYNAMICS**
- **VIVADO PROJECTS | CERTIFICATION**

CAREER OPPORTUNITIES:

- **Front-End VLSI Design Engineer**
- **Physical Design Engineer**
- **ASIC Design Engineer**
- **Verification Engineer**

ELIGIBILITY:

- **12th or equivalent in Science with Physics and Math**
- **2 Years of 3-Years Diploma in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches after class 10th**
- **NSQF Level 3.5 in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches with 1.5 years relevant experience**
- **NSQF Level 3 in Electronics and Communication Engineering/ Electrical Engineering/CS/IT and allied branches with 1.5 years relevant experience**

Course Coordinator:

Anil kumar Shaw | anilshaw@nielit.gov.in
Madhusmita Mohanty(7978702743) | rsmadhusmita.nielit@gmail.com
Anusha Mohanty | (8249910857) | anushamohanty22@gmail.com